

## NONPROVISIONAL PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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## BOX PATENT APPLICATION

NONPROVISIONAL APPLICATION TRANSMITTAL  
RULE §1.53(b)

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Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is the nonprovisional patent application

For (Title): DISPLAY DEVICES WITH INTEGRATED CONTROL ELEMENTS AND METHODS OF MAKING DEVICES

By (Inventors): Masanori IWAHASHI

- ☒ Formal drawings (Figs. 1-13B; 8 sheets) are attached.  
☒ A Declaration and Power of Attorney is filed herewith.  
☒ An assignment of the invention to Kawasaki Steel Corporation is filed herewith.  
☒ An Information Disclosure Statement is filed herewith.  
☐ Entitlement to small entity status is hereby asserted.  
☐ A Preliminary Amendment is filed herewith.  
☐ Please amend the specification by inserting before the first line the sentence --This nonprovisional application claims the benefit of U.S. Provisional Application No. \_\_\_\_\_, filed \_\_\_\_\_.--  
☒ Priority of foreign application No. 11-341650 filed December 1, 1999 in Japan is claimed (35 U.S.C. §119).  
☐ A certified copy of the above corresponding foreign application(s) is filed herewith.  
☐ The filing fee is calculated below:

CLAIMS IN THE APPLICATION AFTER ENTRY OF  
ANY PRELIMINARY AMENDMENT NOTED ABOVE

FOR:	NO. FILED	NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	9 - 20	= 0
INDEP CLAIMS	4 - 3	= 1
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS PRESENTED		

\* If the difference is less than zero, enter "0".

- ☒ Check No. 114013 in the amount of \$790 to cover the filing fee is attached. Except as otherwise noted herein, the Director is hereby authorized to charge any other fees that may be required to complete this filing, or to credit any overpayment, to Deposit Account No. 15-0461. Two duplicate copies of this sheet are attached.

## SMALL ENTITY

RATE	FEE
	\$ 355
x 9 =	\$
x 40 =	\$
+ 135 =	\$
TOTAL	\$

OR  
OR  
OR  
OR  
OR  
OROTHER THAN A  
SMALL ENTITY

RATE	FEE
	\$ 710
x 18 =	\$
x 80 =	\$ 80
+ 270 =	\$
TOTAL	\$ 790

Respectfully submitted,



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>This application is a::  
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**Prior Foreign Applications**

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Country:: Japan  
Priority Claimed:: Yes  
Foreign Application Two::  
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Filing Date::  
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DISPLAY DEVICES WITH INTEGRATED CONTROL ELEMENTS AND  
METHODS OF MAKING DEVICES

BACKGROUND OF THE INVENTION

1. Field of Invention

This invention relates to display devices. This invention also relates to methods of making display devices.

2. Description of Related Art

Transmission-type, liquid-crystal display devices are currently the main stream of the liquid-crystal display devices. These devices require a back-light source on the back of the liquid crystal, the devices consume large amounts of power. In contrast, reflective-type, liquid-crystal display devices display an image by reflecting a light ray incident on the devices from an external light source, such as sunlight or room illumination, and thereby need no back-light source that consumes substantial power. Because of this feature, reflective-type, liquid-crystal displays operate on a substantially smaller power consumption level, and are thus expected to be incorporated in electronics used outdoors, such as portable telephones or portable information terminals.

In addition, reflective-type display devices have already found applications in projectors that use an external light source. A high-resolution feature is required for a display device that is incorporated in a projector. As the display device becomes high in resolution, the size of a unit pixel becomes small. For instance, a diagonally 0.9 inch VGA (video graphics array) display of  $640 \times 480$  (307,200) pixels has a pixel size of about  $28.6 \mu\text{m}$ . A SVGA (super video graphics array) display of  $800 \times 600$  (480,000) pixels has a pixel size of  $22.9 \mu\text{m}$ ; an XGA (extended graphics array) display of  $1024 \times 768$  (786,432) pixels has a pixel size of  $17.8 \mu\text{m}$ ; and a UXGA (ultra high extended graphics array) display of  $1600 \times 1200$  (1,920,000) pixels has a pixel size of  $11.4 \mu\text{m}$ .

Reflective-type, liquid-crystal display devices are constructed by successively stacking a transparent electrode, a liquid crystal, an array of pixel electrodes that reflect incident light from an external light source, and an array of control elements for controlling the alignment of the liquid crystal on the respective pixel electrodes. An example of the construction of reflective-type, liquid-crystal display device is described in U.S. Patent 6,049,132, which was co-invented by this inventor, hereby

Fig. 11 is a circuit diagram showing a portion of a conventional liquid-crystal display device including four rows by four columns of control elements. Each of the control elements 14 includes a switching circuit 28 including a PMOS transistor 24 and an NMOS transistor 26 connected in parallel. The pixel electrode, the liquid crystal, and the transparent electrode are connected between one terminal of the switching circuit 28 and the ground.

Fig. 12 shows the layout of the display device of Fig. 11. Fig. 13A and Fig. 13B respectively show the layout and the circuit diagram of a single pixel. The shape of the unit pixel is typically square, although rectangular-shaped pixels with various aspect ratios may be utilized depending on the intended use. In any case, an aspect ratio of the pixel, or a ratio between the horizontal and vertical pitches of the pixel electrodes, may not be arbitrary determined, but is determined by the intended use. To configure the unit pixel in a square, or a fixed aspect ratio rectangular shape, the area of each control element should also be designed in the square shape, or in the fixed aspect ratio rectangular shape.

In many cases, a liquid-crystal display device requires higher operation voltage than the operation voltage of standard logic devices to drive a liquid crystal. Therefore, it is difficult to miniaturize the control element beneath the layer of the pixel electrode. Further, the square, or the fixed aspect ratio rectangular shape, of the pixel adds to the difficulty of miniaturizing the pixel. That is, when a practically available transistor design rule is implemented, a layout with a minimum space may

require a rectangular area that does not match the shape of the pixel electrode. This presents difficulty in design of the control elements with a minimum area.

#### SUMMARY OF THE INVENTION

Accordingly, it is an object of this invention to provide display devices, which overcome above-described disadvantages of known display devices.

It is a further object of this invention to provide display devices that include an array of control elements corresponding to an array of pixel electrodes even when the shape of an area to form a control element does not match the shape of the pixel electrodes.

Exemplary embodiments of the display devices according to this invention comprise: a semiconductor substrate; an array of control elements formed on the semiconductor substrate; and an array of reflective pixel electrodes arranged in rows and columns, the array of pixel electrodes is stacked over the array of control elements so that each of the control elements controls a state of reflection of a corresponding one of the pixel electrodes. In the display devices, the control elements are arranged in a first direction that makes an angle greater than zero and smaller than a right angle with a direction of the rows, and in a second direction perpendicular to the first direction.

Preferably, the state of reflection is controlled by one of (i) controlling alignment of a liquid-crystal layer disposed over the corresponding one of the pixel electrodes, and (ii) controlling an angle of the corresponding one of the pixel electrodes.

Preferably, embodiments of the display devices further comprise a converter. The converter receives a video signal including a series of image data in an order corresponding to the rows and columns of the pixel electrodes, and converts the order of the series of image data in the received video signal in accordance with correspondences between rows and columns of the array of control elements and the rows and columns of the pixel electrodes. Preferably, the array of control elements includes first drive lines extending in the first direction and second drive lines extending in the second direction.

Preferably, the array of control elements includes row drive lines extending in a direction of the rows of the array of the pixel electrodes, and column drive lines extending in a direction of the columns of the array of the pixel electrodes.

Other exemplary embodiments of the display devices according to this invention comprise: a semiconductor substrate; an array of control elements arranged in rows and columns formed on the semiconductor substrate; and an array of reflective pixel electrodes arranged in rows and columns with a first pitch and a second pitch.

The array of pixel electrodes is stacked over the array of control elements so that each of the control elements controls a state of reflection of a corresponding one of the pixel electrodes. The control elements are arranged in same directions of the rows and columns of the pixel electrodes with a third pitch different from the first pitch, a fourth pitch different from the second pitch, and a product of the first and the second pitches equal to a product of the third and the fourth pitches.

Preferably, the state of reflection is controlled by one of (i) controlling an alignment of a liquid-crystal layer disposed over the corresponding one of the pixel electrodes, and (ii) controlling an angle of the corresponding one of the pixel electrodes.

This invention also provides embodiments of methods of forming the display devices.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 illustrates an embodiment of a display device according to this invention;

Fig. 2 illustrates the positional relationship between a pixel electrode and a control element in a single pixel in the display device of Fig. 1;

Fig. 3 is a layout view of the display device of Fig. 1;

Fig. 4 is a layout view showing the positional relationship between a pixel electrode and a control element in a single pixel in the display device of Fig. 3;

Fig. 5 shows a wiring configuration of a display device according to this invention;

Fig. 6 is a layout view showing a wiring configuration of the display device of Fig. 5;

Fig. 7 is a layout view showing the wiring configuration and the positional relationship between the a pixel electrode and a control element in the display device of Fig. 6;

Fig. 8 shows the positional relationship between pixel electrodes and control elements in two pixels in the display device in an embodiment of the invention;

Figs. 9A and 9B show the configuration of the control elements used in the display device of the invention;

Figs. 10A-10C show the configuration of the pixel electrodes used in the display device in one embodiment of the invention;

Fig. 11 is a circuit diagram showing the construction of a conventional display device;

Fig. 12 is a layout view of the display device of Fig. 11; and

Figs. 13A and 13B show a layout view and a circuit diagram, respectively, of a single pixel of the display device of Fig. 11.

#### DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

This invention was first disclosed in Japanese Patent Application No. 11-341650, which is hereby incorporated by reference in its entirety.

Preferred embodiments of the display devices of this invention are discussed below with reference to the drawings.

Fig. 1 is a conceptual view of an exemplary embodiment of the display devices of this invention. Fig. 2 is a conceptual view showing the positional relationship between a pixel electrode and a control element in a single pixel in the display device of Fig. 1. In these views, squares and rectangles represent pixel electrodes and control elements, respectively. The squares and rectangles shown in Figs. 1 and 2 represent areas to form the pixel electrode and the control element, and the pixel electrode and the control element may actually have various structures formed within respective areas.

Referring to Fig. 1, a display device 10 of this invention includes, on a semiconductor substrate (not shown), an array of a plurality of reflective-type pixel electrodes 12 for reflecting incident light, and an array of a plurality of control elements 14, stacked correspondingly below the plurality of the pixel electrodes 12, for controlling the state of reflection of light from the plurality of the pixel electrodes 12.

In this embodiment, each pixel electrode 12 is formed in a square region. That is, the pixel electrodes 12 are arranged with the same pitch in the direction of rows (i.e., in the horizontal direction in Fig. 1) and in the direction of columns (i.e., in the vertical direction in Fig. 1). The control elements 14 are arranged with different pitches, one pitch in a first direction angled at 45° to the direction of rows, and with



the other pitch in a second direction perpendicular to the first direction. Row drive lines and column drive lines are respectively arranged in the first direction and the second direction. Although each two row drive lines 16 may be paired together as shown in Fig. 11, only a single row drive line is shown in Fig. 1 for each row of the control elements 14 for simplicity.

Referring to Fig. 2, the pixel electrode 12 is formed in a square region having an aspect ratio of 1:1, while the control element 14 is formed in a rectangular region having an aspect ratio of 2:1. The areas of the two regions are equal to each other. In this embodiment, the aspect ratio of the area for forming the control element 14 is 2:1, and the area is angled at 45° to the pixel electrodes 12. Depending on the aspect ratio of the control element 14, the inclination angle of the control element 14 to the pixel electrode 12 may be selected to be any angle within a range greater than zero and smaller than a right angle (i.e., 90°).

Fig. 3 is a layout view of the display device of Fig. 1. Fig. 4 is a layout view showing the positional relationship between a pixel electrode and a control element in a single pixel in the display device of Fig. 3. In these layout views, structures of the pixel electrode and the control element formed within the areas 12 and 14 shown in the views of Figs. 1 and 2 are shown. As shown, nine control elements 14 are respectively arranged for an array of 3 x 3 pixel electrodes 12. The row drive lines 16 and the column drive lines 18 are respectively arranged in the first direction and in the second direction. That is, in this embodiment, the row drive lines 16 extend in the direction of rows of the array of control elements 14, which is inclined from the direction of rows of the array of pixel electrodes 12. Similarly, the column drive lines 18 extend in the direction of columns of the array of control elements 14, which is inclined from the direction of columns of the array of pixel electrodes 12.

Referring to Fig. 4, the rectangular region 14 forming the control element is separated in two square regions by a center line represented by the broken line A. The lower-left one of these square regions 14' is a P-well region, and an NMOS transistor 26 is formed in a central, rectangular area in the lower-left square region. The upper-right square region 14'' is an N-well region, and a PMOS transistor 24 is formed in a central, rectangular area in the upper-right square region. Lines 16' diagonally extending from the upper left to the lower right respectively serve as gates of the transistors. The gates of the plurality of control elements are connected and form the

row drive lines 16. The lower one 18 of two lines extending over the control element 14 diagonally from the upper right to the lower left serves as the column drive line. The PMOS and NMOS transistors are configured with ones of the sources and drains thereof connected to the column drive line, and with the others of the sources and drains thereof connected together to the pixel electrode at the center of the control element 14. The upper one 17 of two lines diagonally extending over the control element 14 from the upper right to the lower left serves as a line for supplying a power supply potential to the well. Fig. 4 shows the line 17 that supplies an N well with a Vdd power supply potential. A line 19 for supplying a P well with a ground potential is arranged within the region of another control element as shown in Fig. 3.

In the square area 12, a square shaped pixel electrode 12a is formed using a substantial portion of the square area 12. In a reflective-type liquid-crystal display device, a fixed metal electrode is formed as the pixel electrode.

In this invention, the array of the control elements 14 is arranged correspondingly to the array of the pixel electrodes 12, even if the control elements 14 fail to match the pixel electrodes 12 in shape. That is, this invention may be implemented in any reflective-type display device, such as a reflective-type, liquid-crystal display device, or in a digital micro-mirror device. In the digital micro-mirror device, a movable mirror is formed as the pixel electrode, and the angle of the movable pixel electrode controls the state of reflection of the pixel electrode. An example of the construction of the digital micro-mirror device is described in U.S. Patent 5,061,049, hereby incorporated by reference in its entirety.

The arrangement of the control elements shown in Fig. 3 is actually different from the arrangement shown in Fig. 1. That is, in Fig. 1, columns of control elements 14 are aligned in the direction of the rows (the first direction). In Fig. 3, adjacent columns of control elements 14 are shifted from each other by a half of the pitch in the direction of the column (the second direction). Either arrangement can be provided in this invention.

To display an image corresponding to a video signal, a display device successively selects one of the row drive lines while scanning the column drive lines, and thereby successively selects control elements at intersections of the selected row drive line and the column drive line. A series of image data is synchronously applied to the column drive lines. In a conventional display device, such as shown in Figs. 11 and 12, row drive lines 16 and column drive lines 18 of the array of control elements

14 extend in the directions of the rows and columns of the array of pixel electrodes 12. That is, rows and columns of the pixel electrodes 12 directly correspond to the row and column drive lines 16 and 18 of the control elements 14. Therefore, by applying video data including a series of image data in an ordinary order, i.e., in an order  
 5 corresponding to the rows and columns of the pixel electrodes, the image is properly displayed.

In a display device 10 according to this invention, such as shown in Figs. 1 and 3, however, the correspondence between the rows and columns of the pixel electrodes 12 and the row and column drive lines 16 and 18 is different from that in the  
 10 conventional display device, such as shown in Figs. 11 and 12. That is, the row drive line 16 extends in the direction of the row of control elements 12, and not in the direction of the row of pixel electrodes 12; and the column drive line 18 extends in the direction of the column of the control elements 14, and not in the direction of the column of the pixel electrode 12. Therefore, control elements 14 are selected, and  
 15 thus pixel electrodes 12 are controlled, by the row and column drive lines 16 and 18 in an order of element 1 → element 2 → element 3 as shown in Fig. 1. Because this scanning order is different from the standard scanning order, the order of the image data should be modified so that the image is properly displayed.

For example, a conversion circuit (not shown) is integrated on the same  
 20 semiconductor substrate. The conversion circuit receives a video signal including a series of image data in an order corresponding to the rows and columns of the pixel electrodes 12, and converts the order in accordance with the correspondences between the row and column drive lines 16 and 18, or the rows and columns of the control elements 14, and the rows and columns of the pixel electrodes 12. Alternatively,  
 25 matching between the order of image data and the order of scanning is achieved by arranging the row drive lines 16 and the column drive lines 18, as explained in detail below with reference to Figs. 5-7.

Fig. 5 shows a conceptual view of another exemplary embodiment of the wiring configuration of the display device of this invention. Referring to Fig. 5, a  
 30 display device 20 includes row drive lines and column drive lines which respectively extend in a zigzag fashion in the directions of rows and columns of the pixel electrodes 12. Each two rows drive lines 16 may be paired as shown in Fig. 11 (Fig. 5 shows only a single row drive line 16 for each control element 14 for simplicity).

The layout configuration is now specifically described. Fig. 6 is a layout view showing another wiring configuration of the display device corresponding to the conceptual view of Fig. 5. Fig. 7 is a layout view showing the wiring configuration and the positional relationship between a pixel electrode 12 and a control element 14 in the display device of Fig. 6. The structure within the control element 14 shown in Fig. 7 is basically identical to that shown in Fig. 4, except that the row drive lines, the column drive lines, and the lines for supplying the power supply potential to the wells extend in a zigzag manner. In Fig. 7, the lower-left square region 14' of the rectangular control element 14 is an N-well region, while the upper-right square region 14' is a P-well region. Fig. 7 also shows a power supply line 19 for supplying a ground potential to the P well. The layout configuration shown in Fig. 6 includes basic cells shown in Fig. 7 and three other types of basic cells formed by mirror-inverting the basic cell shown in Fig. 7.

With this wiring configuration, the illustrated display device 20 successively selects the row drive lines 16 while scanning the column drive lines 18 to display an image corresponding to video data. The control elements 14, and thus the pixel electrodes 12, are selected in the order of element 1 → element 2 → element 3 as shown in Fig. 5, with a standard scanning sequence of the row and column drive lines 16 and 18. Unlike the display device 10 shown in Fig. 1, the display device 20 shown in Fig. 5 needs no operation for modifying the order of image data, and is thus treated in the same way as a conventional display device, such as shown in Figs. 11 and 12.

In the exemplary display devices 10 and 20 of this invention, in principle, a one-to-one correspondence is assured between the pixel electrodes 12 and the control elements 14. However, a plurality of types of cells of control elements 14 may be used in a two-to-two or, more generally, plural-to-plural correspondence with the pixel electrodes 12, and this structure may be periodically arranged. As long as the pixel electrode 12 partly overlaps the corresponding control element 14, there is no other requirement in the positional relationship between the pixel electrode 12 and control element 14.

In the above-described embodiments shown in Figs. 3 and 6, the pixel electrodes 12 are arranged with the same pitch in both the direction of rows and the direction of columns, while the control elements 14 are arranged in an array arranged

in the first and second direction, respectively, at an angle with the directions of rows and columns.

This invention is not limited to this arrangement. The pixel electrodes 12 may alternatively be arranged in an array with the same pitch in the direction of rows and in the direction of columns, while the control elements 14 are arranged in an array with different pitches, one pitch in the direction of rows, and with the other pitch in the direction of columns.

Fig. 8 is a conceptual view showing the positional relationship between pixel electrodes 12 and control elements 14 in two pixels in another exemplary embodiment of the display devices of this invention. As shown, two rows of pixel electrodes 12 are arranged correspondingly to a single row of the control elements 14. The pixel electrodes 12 have an aspect ratio of 1:1 and are arranged in an array with the same pitch in the directions of rows and columns. The control elements 14 of this embodiment are not inclined to the pixel electrodes 12, but have an aspect ratio different from that of the pixel electrode 12. That is, the control elements 14 have an aspect ratio of 4:1 and are arranged in an array with different pitches, one pitch in the direction of rows and the other pitch in the direction of columns.

The area of the square region 12 to form the pixel electrode is equal to the area of the rectangular region 14 to form the control element, and the pixel electrodes 12 partly overlap the corresponding control elements 14. In other words, the square of the arrangement pitch of the pixel electrodes 12 is equal to the product of the arrangement pitches of the control elements 14 in the directions of the rows and the columns. If the region to form the pixel electrode 12 has a rectangular shape, the product of the arrangement pitches of the pixel electrodes 12 in the directions of rows and columns is equal to the product of the arrangement pitches of the control elements 14 in the directions of rows and columns.

A combination of the two pixel electrodes 12 and the two control elements 14 shown in Fig. 8 is treated as a unit. Such units are periodically arranged in an array to construct the display device of this invention. As shown, the aspect ratio of the control element 14 is 4:1, and the pixel electrodes 12 and the control elements 14 combined in a two-to-two correspondence are periodically arranged.

However, this invention is not limited to only this arrangement. Rather, depending on the aspect ratio of the control element 14, any suitable plural-to-plural correspondence between the pixel electrodes and the control elements can be adopted.

In this invention, the shape of the area to form the pixel electrode 12 is not limited to a square, and the shape of the area to form the control element 14 is not limited to a rectangular shape. Referring to Figs. 9A-10C, variations of configurations of the pixel electrodes 12 and the control elements 14 are shown. In Figs. 9A and 9B, solid lines represent areas to form control elements 14. In Figs. 10A-10C, solid lines represent areas to form pixel electrodes 12.

The array of the control elements 14 includes any array, which may be regarded as an array of substantially rectangular shapes, even if the area to form the control element is not completely rectangular. Referring to Figs. 9A and 9B, the areas surrounded by solid lines are variations of the rectangular-shaped unit cell areas represented by broken lines. Arrangements of control elements 14 shown in Figs. 9A and 9B fall within the scope of this invention. There is no particular limitation on the shape of the control element 14. For example, a combination of triangles or pentagons may be periodically repeated.

Also, arrangements of pixel electrodes shown in Figs. 10A-10C all fall within the scope of this invention. Shapes of the areas to form pixel electrodes represented by solid lines are mere variations of squares represented by broken lines.

The conventional switching circuit shown in Fig. 11 may be employed as the circuit arrangement of the control element in embodiments of this invention. The layout configuration of the control element 14 is not limited to any particular configuration and may be freely modified. In addition to the switching circuit, the control elements 14 may also include a capacitor.

As discussed above, in the display devices of this invention, an array of control elements is arranged to correspond to an array of pixel electrodes arranged in an upper layer even when miniaturization of the control elements does not permit the shape of the control elements in a lower layer to match the shape of the pixel electrodes. This invention thus provides a compact and high-resolution reflective-type display device, which is thus easy to design, and is appropriate for use in projectors and various other devices.

This invention is not limited to the above-described embodiments, and various changes and modifications may be incorporated in this invention without departing from the scope of this invention.

WHAT IS CLAIMED IS:

1. A display device, comprising:  
a semiconductor substrate;  
an array of control elements on the semiconductor substrate; and  
5 an array of reflective pixel electrodes arranged in rows and columns,  
the array of pixel electrodes is stacked over the array of control elements so that each  
of the control elements controls a state of reflection of a corresponding one of the  
pixel electrodes,  
wherein the control elements are arranged (i) in a first direction that  
10 makes an angle greater than zero and smaller than 90° with a direction of the rows,  
and (ii) in a second direction perpendicular to the first direction.
2. The display device according to claim 1, wherein the state of reflection  
is controlled by one of (i) controlling alignment of a liquid-crystal layer disposed over  
the corresponding one of the pixel electrodes, and (ii) controlling an angle of the  
15 corresponding one of the pixel electrodes.
3. The display device according to claim 1, further comprising a converter  
that (i) receives a video signal including a series of image data in an order  
corresponding to the rows and columns of the pixel electrodes, and (ii) converts the  
order of the series of image data in the received video signal in accordance with  
20 correspondences between rows and columns of the array of control elements and the  
rows and columns of the pixel electrodes.
4. The display device according to claim 3, wherein the array of control  
elements includes first drive lines extending in the first direction and second drive  
lines extending in the second direction.
5. The display device according to claim 1, wherein the array of control  
25 elements includes row drive lines extending in a direction of the rows of the array of  
the pixel electrodes, and column drive lines extending in a direction of the columns of  
the array of the pixel electrodes.
6. A display device, comprising:  
a semiconductor substrate;  
an array of control elements arranged in rows and columns formed on  
the semiconductor substrate; and  
an array of reflective pixel electrodes arranged in rows and columns  
30 with a first pitch and a second pitch, the array of pixel electrodes is stacked over the

array of control elements so that each of the control elements controls a state of reflection of a corresponding one of the pixel electrodes;

wherein the control elements are arranged in same directions as the rows and columns of the pixel electrodes with (i) a third pitch different from the first pitch, (ii) a fourth pitch different from the second pitch, and (iii) a product of the first and the second pitches equal to a product of the third and the fourth pitches.

7. The display device according to claim 6, wherein the state of reflection is controlled by (i) one of controlling an alignment of a liquid-crystal layer disposed over the corresponding one of the pixel electrodes, and (ii) controlling an angle of the corresponding one of the pixel electrodes.

8. A method of making a display device, comprising:

forming an array of control elements on the semiconductor substrate;

and

forming an array of reflective pixel electrodes arranged in rows and columns, the array of pixel electrodes being stacked over the array of control elements so that each of the control elements controls a state of reflection of a corresponding one of the pixel electrodes,

wherein the control elements are arranged (i) in a first direction that makes an angle greater than zero and smaller than  $90^\circ$  with a direction of the rows, and (ii) in a second direction perpendicular to the first direction.

9. A method of making a display device, comprising:

forming an array of control elements arranged in rows and columns on a semiconductor substrate; and

forming an array of reflective pixel electrodes arranged in rows and columns with a first pitch and a second pitch, the array of pixel electrodes being stacked over the array of control elements so that each of the control elements controls a state of reflection of a corresponding one of the pixel electrodes;

wherein the control elements are arranged in same directions as the rows and columns of the pixel electrodes with (i) a third pitch different from the first pitch, (ii) a fourth pitch different from the second pitch, and (iii) a product of the first and the second pitches equal to a product of the third and the fourth pitches.



ABSTRACT OF THE DISCLOSURE

A display device includes an array of control elements formed on a semiconductor substrate, and an array of reflective-type pixel electrodes stacked over the array of the control elements, so that each of the control elements control a state of reflection of a corresponding one of the pixel electrodes. The array of the pixel electrodes is arranged in rows and columns. In embodiments, the array of the control elements is arranged in a first direction that makes an angle greater than zero and smaller than  $90^\circ$  with the direction of rows, and in a second direction that is perpendicular to the first direction. The display device has an array of control elements corresponding to the array of pixel electrodes even when the control elements do not match the pixel electrodes in shape. This invention also provides methods of making the display devices.

FIG. 1

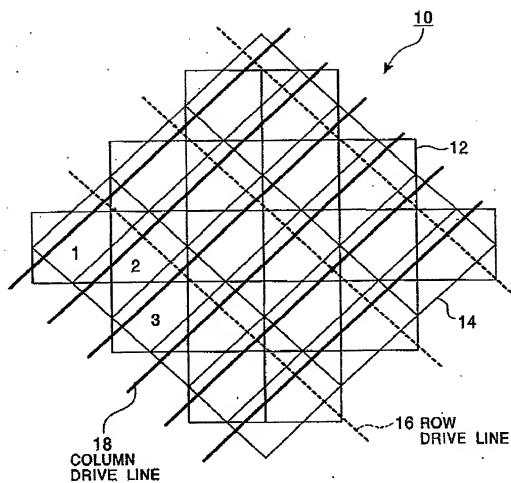


FIG. 2

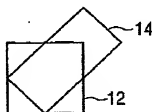


FIG. 3

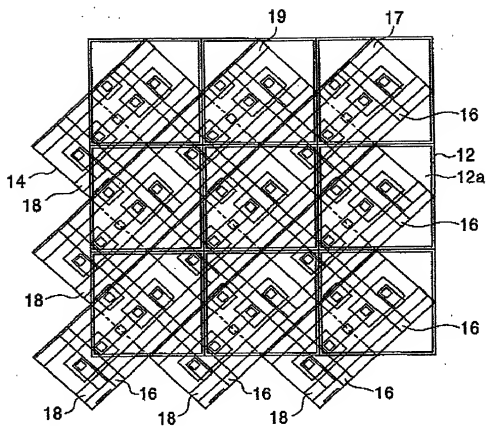


FIG. 4

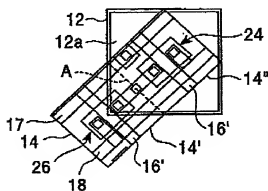


FIG. 5

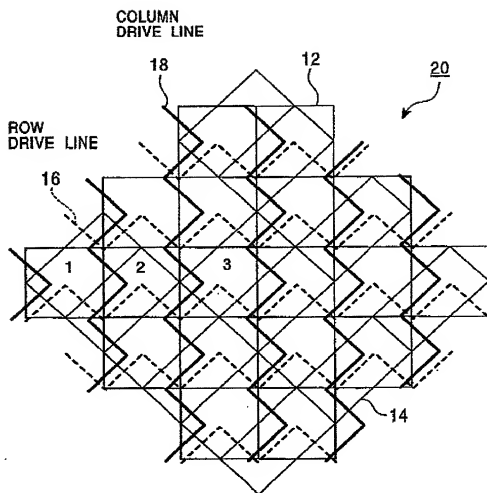


FIG. 6

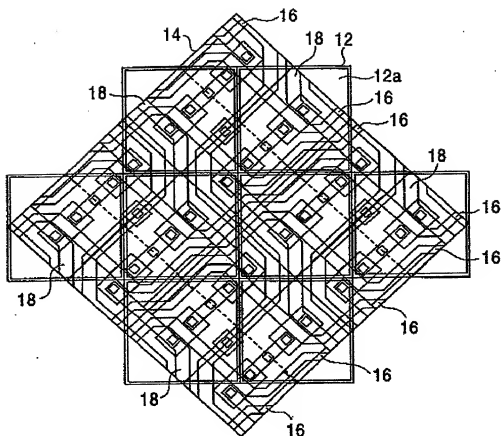


FIG. 7

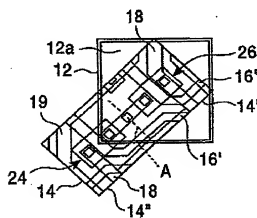


FIG. 8

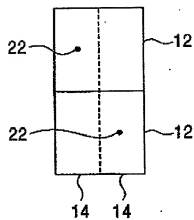


FIG. 9A

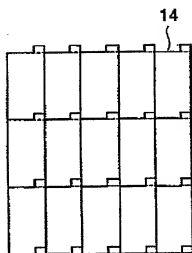


FIG. 9B

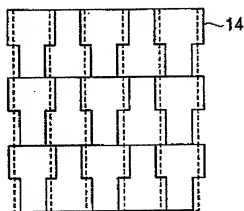


FIG. 10A

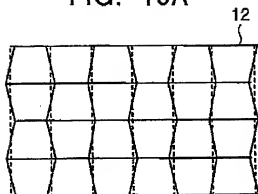


FIG. 10B

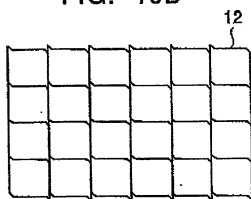


FIG. 10C

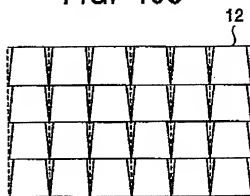


FIG. 11

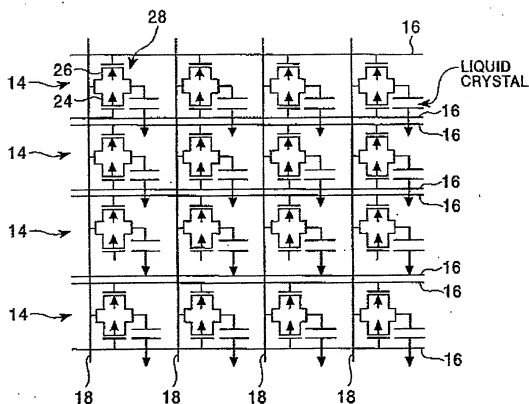




FIG. 12

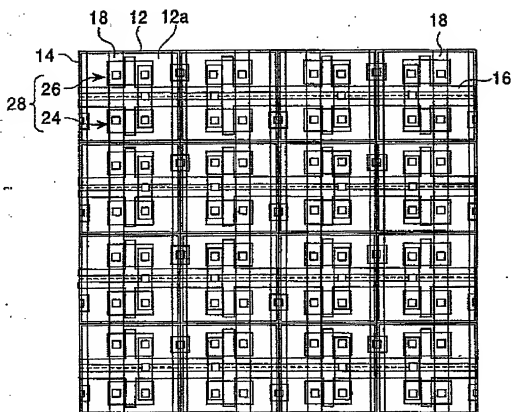


FIG. 13A

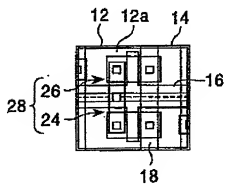
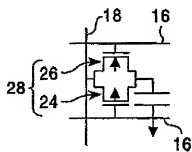


FIG. 13B



# Declaration and Power of Attorney for Patent Application

## 特許出願宣言書兼委任状

Japanese Language Declaration

私は、下欄に氏名を記載した発明者として、以下のとおり宣言する：

私の住所、郵便先および国籍は、下欄に氏名に就いて記載したとおりであり、下記名称の発明に關し、特許請求の範圍に記載した特許を本める主題の本発明の、最初にして唯一の発明者である（一人の氏名のみが下欄に記載されている場合）か、もしくは本発明の、最初にして共同の発明者である（複数の氏名が下欄に記載されている場合）と信じ、

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name. I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

### DISPLAY DEVICES WITH INTEGRATED CONTROL ELEMENTS

### AND METHODS OF MAKING DEVICES

その明細書を  
(添付するものにチェック)  
☒ここに添付する。

the specification of which  
(check one)  
☒is attached hereto.

☐ 年 月 日に

☐ was filed on \_\_\_\_\_ as

出願番号第 \_\_\_\_\_ として提出され、

Application Serial No. \_\_\_\_\_

年 月 日に補正し、  
(補正する場合)

and was amended on \_\_\_\_\_  
(if applicable)

私は、前記のとおり補正した特許請求の範圍を含む特許明細書の内容を検討し、理解したこと全該述する。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦施行規則第 37 章第 1 条第 56 項に従い、本願の特許性の有無について重要な情報を開示すべき義務を有することを認める。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

私は、米国法第 35 章第 119 条に基づく下記の外国特許出願もしくは発明者権出願の外国優先権利益を主張し、さらに優先権の主張に係わる露荷出願の提出日前の提出日を有する外国特許出願もしくは発明者権出願およびもしくは米国仮出願を以下に明記する：

I hereby claim foreign priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate listed below and/or any U.S. provisional application(s) listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior foreign and/or provisional applications  
先行外国出願/仮出願

Priority claimed  
優先権の主張

11-341650 (Number/番号)	Japan (Country/国名)	1 December 1999 (Day/Month/Year Filed/提出年月日)	<input checked="" type="checkbox"/> (Yes/はい)	<input type="checkbox"/> (No/いいえ)
 (Number/番号)	 (Country/国名)	 (Day/Month/Year Filed/提出年月日)	<input type="checkbox"/> (Yes/はい)	<input type="checkbox"/> (No/いいえ)
 (Number/番号)	 (Country/国名)	 (Day/Month/Year Filed/提出年月日)	<input type="checkbox"/> (Yes/はい)	<input type="checkbox"/> (No/いいえ)
 (Number/番号)	 (Country/国名)	 (Day/Month/Year Filed/提出年月日)	<input type="checkbox"/> (Yes/はい)	<input type="checkbox"/> (No/いいえ)

私は、米国法第 35 条第 120 条に基づく下記の米国特許出願の利益を主張し、本願の特許請求の範囲各項に記載の主題が米国法第 35 条第 112 条の第 1 段落に規定の影響で先の米国出願に開示されていない程度において、先の出願の提出日と本願の国内提出日もしくは PCT 国際出願提出日の間に公表された連邦施行規則第 37 条第 1 条第 56 項に記載の重要な情報を開示すべき義務を有することを認める。

I hereby claim the benefit under Title 35, United States code, §120 of any United States application(s) listed below and, in so far as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112. I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No./出願番号)

(Filing Date/提出日)

(Status: Patented, Pending, abandoned/  
状況: 特許成立、係属中、放棄済み)

(Application Serial No./出願番号)

(Filing Date/提出日)

(Status: Patented, Pending, abandoned/  
状況: 特許成立、係属中、放棄済み)

私は、ここに自己の知識にもとづいて行った陳述がすべて真実であり、自己の有する情報および信ずるところに従って行った陳述が真実であると信じ、さらに故意に虚偽の陳述等を行った場合、米国法第 18 条第 1001 条により、罰金もしくは禁錮に処せられるか、またはこれらの刑が併科され、またかかる故意による虚偽の陳述が本願ないし本願に対して付与される特許の有効性を損なうことがあることを認識して、以上の陳述を行ったことを宣言する。

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

委任状：私は下記宛明者として、以下の代理人をここに委任し、本願の手続を進行すること並びにこれに關する一切の行為を特許商標庁に対して行うことを委任する。（代理人氏名および登録番号を明記のこと）

**POWER OF ATTORNEY:** As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (*list name and registration number*)

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Residence/住所	
Citizenship/国籍	
Post Office Address/郵便局先	